

SILICON TRANSISTORS 2SA1221, 1222

PNP SILICON EPITAXIAL TRANSISTOR FOR LOW-FREQUENCY POWER AMPLIFIERS

FEATURES

- Ideal for use of high withstanding voltage current such as TV vertical deflection output, audio output, and variable power supplies.
- · Complementary transistor with 2SC2958 and 2SC2959

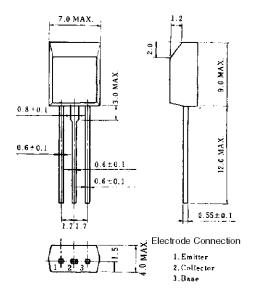
VCEO = 140 V: 2SA1221/2SC2958 VCEO = 160 V: 2SA1222/2SC2959

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Parameter	Symbol	Ratings	Unit	
Collector to base voltage	Vcво	-160	V	
Collector to emitter voltage	Vceo	V _{CEO} -140/-160		
Emitter to base voltage	to base voltage V _{EBO}		V	
Collector current (DC)	ent (DC) Ic(DC) -500		mA	
Collector current (pulse)	IC(pulse)*	-1.0	Α	
Total power dissipation	Рт	1.0	W	
Junction temperature	Tj	150		
Storage temperature	T _{stg}	-55 to +150	°C	

^{*} PW \leq 10 ms, duty cycle \leq 50%

PACKAGE DRAWING (UNIT: mm)



ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	$V_{CB} = -100 \text{ V}, I_E = 0$			-200	nA
Emitter cutoff current	ІЕВО	$V_{EB} = -5.0 \text{ V}, \text{ Ic} = 0$			-200	nA
DC current gain	hfe **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -100 \text{ mA}$	100	150	400	
DC base voltage	V _{BE} **	$V_{CE} = -5.0 \text{ V}, \text{ Ic} = -20 \text{ mA}$	-0.6	-0.64	-0.7	V
Collector saturation voltage	V _{CE(sat)} **	$I_C = -1.0 \text{ A}, I_B = -0.2 \text{ A}$		-0.6	-0.9	V
Base saturation voltage	V _{BE(sat)} **	$I_C = -1.0 \text{ A}, I_B = -0.2 \text{ A}$		-1.1	-0.3	V
Output capacitance	Cob	$V_{CB} = -10 \text{ V}, \text{ Ie} = 0, \text{ f} = 1.0 \text{ MHz}$		24	40	pF
Gain bandwidth product	f⊤	$V_{CE} = -10 \text{ V}, \text{ Ie} = 20 \text{ mA}$	30	45		MHz

^{**} Pulse test PW \leq 350 μ s, duty cycle \leq 2% per pulsed

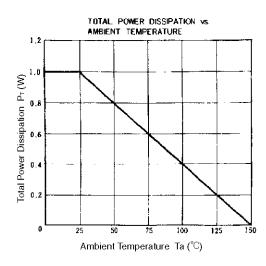
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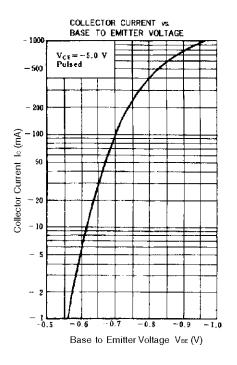


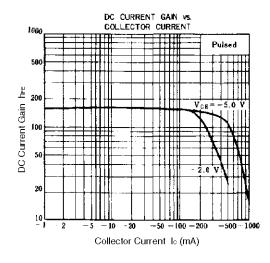
hfe CLASSIFICATION

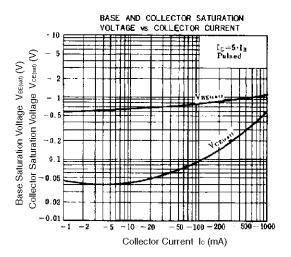
Marking	М	L	K
hfE	100 to 200	160 to 320	200 to 400

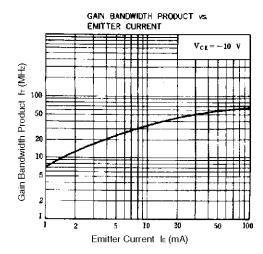
TYPICAL CHARACTERISTICS (Ta = 25°C)

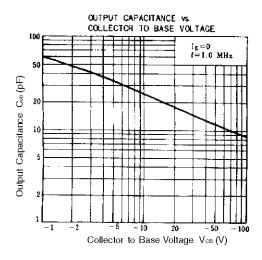












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